

IN THE CLAIMS:

The text of all pending claims are set forth below. Cancelled and withdrawn claims are indicated with claim number and status only. The claims as listed below show added text with underlining and deleted text with ~~strikethrough~~. The status of each claim is indicated with one of (original), (currently amended), (previously presented), (cancelled), (withdrawn), (new) and (not entered).

Please REPLACE claims 1, 3, 6-7, 10-11, 19-20, 23-24, 34-35 and 38-39 in accordance with the following:

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1. (currently amended) A driving method for a display apparatus, wherein a frequency of a clock signal, used to drive a display panel, is continuously varied ~~in a frequency of the clock signal~~, and said display panel is driven with said frequency varying clock signal.
2. (previously presented) The driving method for a display apparatus as claimed in claim 1, wherein said clock signal used to drive said display panel is a source clock signal of said display apparatus.
3. (currently amended) The driving method for a display apparatus as claimed in claim 1, wherein the frequency of said clock signal used to drive said display panel continuously varies within a range of plus or minus 1 percent of a reference frequency.
4. (original) The driving method for a display apparatus as claimed in claim 1, wherein said display apparatus is a plasma display apparatus.
5. (previously presented) The driving method for a display apparatus as claimed in claim 1, wherein a control of said clock signal used to drive said display panel is performed during a quiescent period.
6. (currently amended) A driving method for a display apparatus having a display panel, wherein a peak noise output of the display panel is reduced by sequentially switching a clock signal, used to drive the display panel, between at least two frequencies in accordance with time conditions.
7. (currently amended) ~~The~~ A driving method for a display apparatus as claimed in

~~claim 6 having a display panel, wherein a peak noise output of the display panel is reduced by sequentially switching a clock signal, used to drive the display panel, between at least two frequencies, wherein two frequencies lying within plus or minus 1 percent of a reference frequency are being set for said clock signal used to drive said display panel.~~

8. (original) The driving method for a display apparatus as claimed in claim 6, wherein said display apparatus is a plasma display apparatus.

9. (previously presented) The driving method for a display apparatus as claimed in claim 6, wherein a control of said clock signal used to drive said display panel is performed during a quiescent period.

10. (currently amended) A driving method for a display apparatus, wherein drive waveforms for a display panel are provided corresponding to at least two frequencies, and said display panel is driven by sequentially time switching of an output drive waveform between said drive waveforms corresponding to said at least two frequencies in accordance with time conditions.

11. (currently amended) ~~The~~ A driving method for a display apparatus as claimed in claim 10, wherein drive waveforms for a display panel are provided corresponding to at least two frequencies, and said display panel is driven by sequentially switching an output drive waveform between said drive waveforms corresponding to said at least two frequencies, ~~wherein~~ said drive waveforms for said display panel are provided corresponding to two frequencies lying within plus or minus 1 percent of a reference frequency.

12. (original) The driving method for a display apparatus as claimed in claim 10, wherein said display apparatus is a plasma display apparatus.

13. (previously presented) The driving method for a display apparatus as claimed in claim 10, wherein a control of said clock signal used to drive said display panel is performed during a quiescent period.

14. (previously presented) A display apparatus, comprising:
a clock generating circuit;
a drive waveform generating circuit generating a drive waveform by using a clock signal from said clock generating circuit having a continuously varying frequency; and
a display panel displaying an image in accordance with said drive waveform, wherein said drive waveform generating circuit drives said display panel by outputting the drive waveform having a varying frequency in accordance with said frequency varying clock signal.

15. (previously presented) The display apparatus as claimed in claim 14, wherein said clock generating circuit generates a source clock signal of said display apparatus.

16. (previously presented) The display apparatus as claimed in claim 14, wherein said clock signal whose frequency varies continuously is within a range of plus or minus 1 percent of a reference frequency.

17. (original) The display apparatus as claimed in claim 14, wherein said display apparatus is a plasma display apparatus.

18. (previously presented) The display apparatus as claimed in claim 14, wherein during a quiescent period, said clock generating circuit performs a control of said clock signal used to drive said display panel.

19. (currently amended) A display apparatus, comprising:
a clock generating circuit;
a drive waveform generating circuit generating a drive waveform by using a clock signal from said clock generating circuit having a ~~sequentially switched~~ frequency switched between at least two frequencies in accordance with time conditions; and
a display panel displaying an image in accordance with said drive waveform, wherein said drive waveform generating circuit drives said display panel by outputting the drive waveform having a switched frequency in accordance with said ~~sequentially switched~~ clock signal.

20. (currently amended) ~~The~~ A display apparatus, as claimed in claim 19 comprising:
a clock generating circuit;

a drive waveform generating circuit generating a drive waveform by using a clock signal from said clock generating circuit having a sequentially switched frequency switched between at least two frequencies; and

a display panel displaying an image in accordance with said drive waveform, wherein said drive waveform generating circuit drives said display panel by outputting the drive waveform having a switched frequency in accordance with said sequentially switched clock signal,

wherein said clock signal sequentially switched between two frequencies is within plus or minus 1 percent of a reference frequency.

21. (original) The display apparatus as claimed in claim 19, wherein said display apparatus is a plasma display apparatus.

22. (previously presented) The display apparatus as claimed in claim 19, wherein during a quiescent period, said clock generating circuit performs a control of said clock signal used to drive said display panel.

23. (currently amended) A display apparatus, comprising;
a clock generating circuit;

a drive waveform generating circuit generating a drive waveform by using a clock signal from said clock generating circuit having a sequentiallytime switched frequency switched between at least two frequencies in accordance with time conditions; and

a display panel displaying an image in accordance with said drive waveform, wherein said drive waveform generating circuit drives said display panel by sequentially time switching of an output drive waveform between drive waveforms corresponding to the at least two frequencies.

24. (currently amended) The A display apparatus, as claimed in claim 23comprising;
a clock generating circuit;
a drive waveform generating circuit generating a drive waveform by using a clock signal from said clock generating circuit having a sequentially switched frequency switched between at least two frequencies; and

a display panel displaying an image in accordance with said drive waveform, wherein said drive waveform generating circuit drives said display panel by sequentially switching an

output drive waveform between drive waveforms corresponding to at least two frequencies,
wherein said drive waveform generating circuit sequentially switches said output drive waveform between drive waveforms corresponding to two frequencies lying within plus or minus 1 percent of a reference frequency.

25. (original) The display apparatus as claimed in claim 23, wherein said display apparatus is a plasma display apparatus.

26. (previously presented) The display apparatus as claimed in claim 23, wherein during a quiescent period, said clock generating circuit performs a control of said clock signal used to drive said display panel.

27. (previously presented) A driving method for a display apparatus, comprising:
continuously varying a frequency of a clock signal; and
driving a display panel with said continuously varying frequency clock signal.

28. (previously presented) The driving method for a display apparatus as claimed in claim 27, further comprising:

using the continuously varying frequency clock signal as a source clock signal of said display apparatus.

29. (previously presented) The driving method for a display apparatus as claimed in claim 27, wherein the continuous varying of the frequency of the clock signal is within a range of plus or minus 1 percent of a reference frequency.

30. (previously presented) The driving method for a display apparatus as claimed in claim 27, wherein said display apparatus is a plasma display apparatus.

31. (previously presented) The driving method for a display apparatus as claimed in claim 27, further comprising:

performing a control of said clock signal used to drive said display panel during a quiescent period.

32. (previously presented) The driving method for a display apparatus as claimed in claim 27, wherein said driving of the display panel reduces peak values of noise emitted by the display panel.

33. (previously presented) The driving method for a display apparatus as claimed in claim 27, wherein said driving of the display panel spreads out frequencies of noise emitted by the display panel.

34. (currently amended) A driving method for a display apparatus, comprising:
sequentially-switching a clock signal between at least two frequencies in accordance with time conditions; and
driving a display panel by with said sequentially-switched clock signal to reduce peak values of noise emitted by the display panel.

35. (currently amended) A driving method for a display apparatus, comprising:
providing drive waveforms for a display panel corresponding to a plurality of frequencies;
sequentially-switching an output drive waveform between said drive waveforms corresponding to said plurality of frequencies in accordance with time conditions; and
driving said display panel by said drive waveforms corresponding to said plurality of frequencies.

36. (previously presented) A display apparatus including a display panel to display an image, comprising:
a clock generating circuit to generate a clock signal having a continuously varying frequency; and
a drive waveform generating circuit generating a drive waveform having a frequency varying in accordance with said frequency varying clock signal and driving the display panel in accordance with the generated drive waveform.

37. (previously presented) A display apparatus including a display panel to display an image comprising:
a clock generating circuit to generate a clock signal based on a spread-type clock oscillator; and

a drive waveform generating circuit generating a drive waveform having a frequency varying in accordance with the generated clock signal and driving the display panel in accordance with the generated drive waveform.

38. (currently amended) A display apparatus including a display panel to display an image, comprising:

a clock generating circuit to generate a clock signal sequentially switched between a plurality of frequencies in accordance with time conditions;

a drive waveform generating circuit generating a drive waveform having a frequency switched in accordance with said sequentially switched clock signal and driving the display panel in accordance with the generated drive waveform.

39. (currently amended) A display apparatus including a display panel to display an image, comprising:

a clock generating circuit; and

a drive waveform generating circuit generating a drive waveform by sequentially switching an output drive waveform between drive waveforms corresponding to a plurality of frequencies in accordance with time conditions, and driving the display panel in accordance with the generated drive waveform.

40. (new) The driving method for a display apparatus as claimed in claim 6, wherein the switching of the clock signal between the at least two frequencies in accordance with the time conditions comprises:

periodically switching the clock signal to reduce the peak noise output of the display panel.

41. (new) The driving method for a display apparatus as claimed in claim 10, wherein the time switching of the output drive waveform between said drive waveforms corresponding to said at least two frequencies in accordance with the time conditions comprises:

periodically switching the output drive waveform between said drive waveforms corresponding to said at least two frequencies to reduce the peak noise output of the display panel.

42. (new) The display apparatus as claimed in claim 19, wherein the clock signal switched between said at least two frequencies in accordance with the time conditions is periodically switched between said at least two frequencies to reduce the peak noise output of the display panel.

43. (new) The display apparatus as claimed in claim 23, wherein the clock signal time switched between said at least two frequencies in accordance with the time conditions is periodically time switched between said at least two frequencies to reduce the peak noise output of the display panel.

44. (new) The driving method for a display apparatus as claimed in claim 34, wherein the switching of the clock signal between said at least two frequencies in accordance with the time conditions comprises:

periodically switching the clock signal between said at least two frequencies to reduce the peak noise output of the display panel.

45. (new) The driving method for a display apparatus as claimed in claim 35, wherein the switching of the output drive waveform between said drive waveforms corresponding to said plurality of frequencies in accordance with the time conditions comprises:

periodically switching the output drive waveform between said drive waveforms corresponding to said plurality of frequencies to reduce the peak noise output of the display panel.

46. (new) The display apparatus as claimed in claim 38, wherein the clock signal switched between the plurality of frequencies in accordance with the time conditions is periodically switched between the plurality of frequencies to reduce the peak noise output of the display panel.

47. (new) The display apparatus as claimed in claim 23, wherein the output drive waveform switched between the drive waveforms corresponding to the plurality of frequencies in accordance with time conditions is periodically switched between the plurality of frequencies to reduce the peak noise output of the display panel.